CLAIM LISTING

1. (Previously Presented) A transport stream feeder for verifying a video decoder, said transport stream feeder comprising:

a digital input/output card, said digital input/output card comprising:

a first memory for storing a reference video;

a processor for encoding the reference video, providing the encoded reference video to the video decoder, and receiving a decoded reference video from the video decoder and writing the decoded reference video to the second memory; and

the second memory for storing the decoded reference video.

2. (Original) The transport stream feeder of claim 1, wherein the digital input/output card further comprises:

an interface for transmitting the reference video to the video decoder.

3. (Currently Amended) The transport stream feeder of claim 1, further comprising:

an adapter for connecting the digital input/output card to the video decoder, said adapter and capable of removing the digital input/output card from the video decoder.

4. (Original) The transport stream feeder of claim 1, wherein the digital input/output card further comprises:

a third memory for storing a plurality of instructions executable by the processor, wherein execution of the plurality of instructions by the processor causes:

encoding the reference video;

transmitting the encoded reference video;

receiving the decoded reference video from the video decoder; and

storing the decoded reference video from the video decoder in the second memory.

5. (Original) The transport stream feeder of claim 1, wherein the encoded reference video comprises an MPEG transport stream.

6. (Currently Amended) A transport stream feeder for verifying a video decoder, said transport stream feeder comprising:

a digital input/output card, said digital input/output card comprising:

a first memory ~~for storing~~ operable to store a reference video;

a processor connected to the first memory, the processor ~~for encoding~~ operable to encode the reference video, ~~providing~~ provide the encoded reference video to the video decoder, and ~~receiving~~ receive a decoded reference video from the video decoder, and ~~writing~~ write the decoded reference video to a second memory; and

the second memory connected to the processor, the second memory ~~for storing~~ operable to store the decoded reference video.

7. (Currently Amended) The transport stream feeder of claim 6, wherein the digital input/output card further comprises:

an interface operatively coupled to the processor~~, the interface for~~ to transmit~~ting~~ the reference video to the video decoder.

8. (Currently Amended) The transport stream feeder of claim 6, further comprising:

an adapter operatively coupled to the interface~~, the adapter for~~ and for operably coupling the digital input/output card to the video decoder and capable of disconnecting the input/output card to the video decoder.

9. (Currently Amended) The transport stream feeder of claim 6, wherein the digital input/output card further comprises:

a third memory operatively coupled to the processor, the third memory ~~for storing~~ operable to store a plurality of instructions executable by the processor, wherein execution of the plurality of instructions by the processor causes:

encoding the reference video;

transmitting the encoded reference video;

receiving the decoded reference video from the video decoder; and

storing the decoded reference video from the video decoder in the second memory.

10. (Original) The transport stream feeder of claim 6, wherein the encoded reference video comprises an MPEG transport stream.

11. (New) The transport stream feeder of claim 1, wherein:

the processor provides the encoded video data directly to the video decoder.

12. (New) The transport stream feeder of claim 1, wherein:

the processor provides the encoded video data to the video decoder immediately after encoding.

13. (New) The transport stream feeder of claim 1, wherein:

the first memory stores the entire reference video while the second memory stores the entire decoded video.